

TRF3765 REF_IN Impedance Application Note

Pete Hanish

High Speed Products

ABSTRACT

The REF_IN pin of the TRF3765 is designed to accept a wide range of clock frequency inputs. As with all high frequency inputs, the REF_IN input impedance is frequency dependent. This document provides measurement data for the REF_IN input impedance.

1 Measurement Conditions

Measurements were gathered using a TRF3765EVM board in default configuration with power applied.

An Agilent E5071B was configured to measure S11 parameters. A port extension using an unassembled board set the reference plane at the REF_IN terminal. All measurements are referenced to a 50 Ω characteristic impedance.

2 Measurement Results

Table 1 shows equivalent parallel R and C from impedance measurements at selected frequencies. Graphs showing resistance and capacitance over frequency are included as Figure 1 and Figure 2.

Table 1. Impedance Measurements at Selected Frequencies.

Frequency (MHz)	Shunt Resistance (Ω)	Shunt Capacitance (pF)
1	15k	10
10	2.5k	2
31	320	1.7
61	123	1.7
156	71	1.7
350	44	1.6

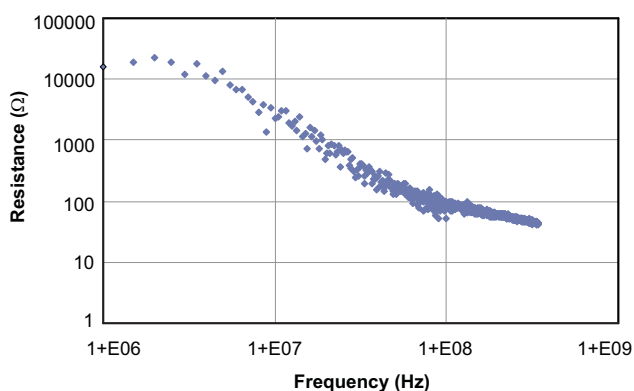


Figure 1. REF_IN Shunt Resistance vs. Frequency

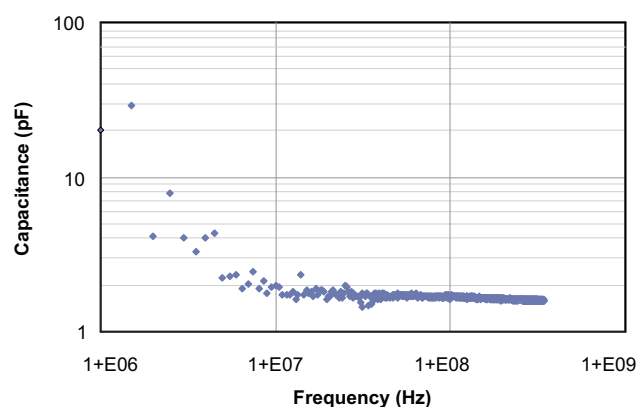


Figure 2. REF_IN Shunt Capacitance vs. Frequency

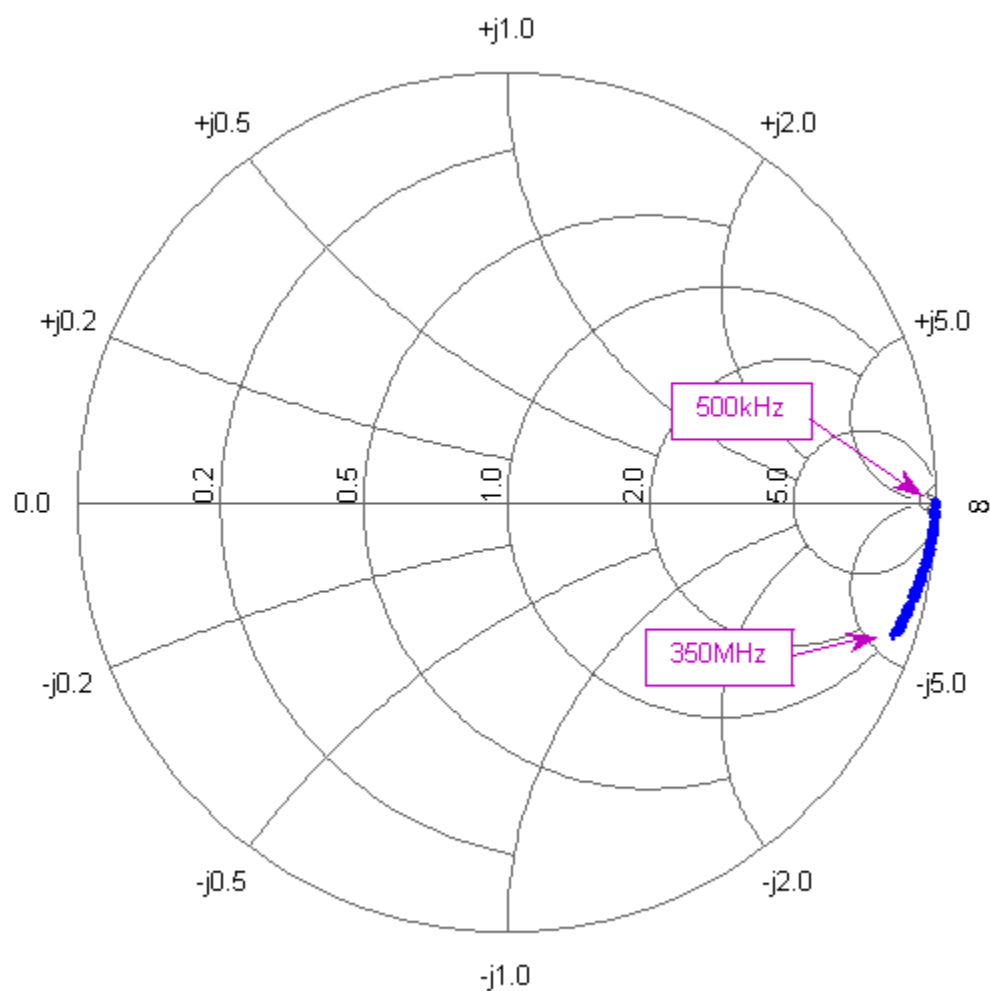


Figure 3. Figure 3. Smith Chart Showing Complex Reflection Coefficient

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